

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 57454-964		SERIAL NO. Divisional of Appl. No. 09/832,025	
				APPLICANT Hideto HIDAKA			
				FILING DATE July 09, 2003		GROUP 2824	

U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number <small>Number-Kind Code₂ (if known)</small>	Publication Date <small>MM-DD-YYYY</small>	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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						<input type="checkbox"/> Yes <input type="checkbox"/> No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), data, page(s), volume-issue number(s), publisher, city and/or country where published.
VTN		Roy Schaeferlein, et al. "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell", ISSCC Digest of Technical Papers, TA7.2, February 2000, pp. 128-129, 94-95 and 409.
VTN		M. Durlam et al., "Nonvolatile RAM Based on Magnetic Tunnel Junction Elements", ISSCC Digest of Technical Papers, TA7.3, February 2000, pp. 130-131, 96-97, 410-411.

EXAMINER 	DATE CONSIDERED 7/19/04
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